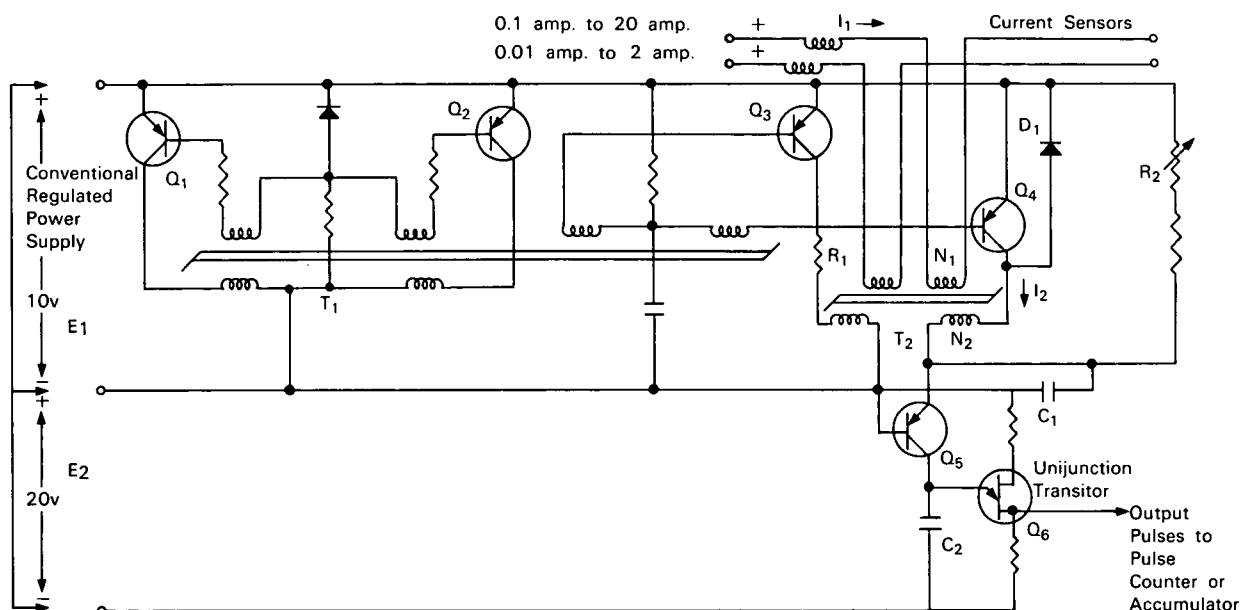


NASA TECH BRIEF



NASA Tech Briefs are issued by the Technology Utilization Division to summarize specific technical innovations derived from the space program. Copies are available to the public from the Clearinghouse for Federal Scientific and Technical Information, Springfield, Virginia, 22151.

Electronic Ampere-Hour Integrator is Accurate to One Percent



The problem: Normally, ampere-hour integrators are mechanical devices, such as a ball and disc, which operate on information from a recorder. They have the inherent disadvantages of large physical size, dependence on the accuracy of the recorder, and mechanical wear which results in drift requiring frequent calibration.

The solution: A solid state, dual-range electronic ampere-hour integrator based on current-to-frequency conversion. The integrator operates on low power, and is accurate to within 1 percent from a 10 milli-ampere rate to a 20 ampere rate.

How it's done: The circuit shown is based on the reflected impedance of transformer T₂. A square wave

oscillator circuit, using transistors Q₁ and Q₂, alternately drives transistors Q₃ and Q₄. Each time transistor Q₃ is turned "on", saturable transformer T₂ is reset to its negative saturation state. When transistor Q₄ is turned "on", the reflected current from the sensing circuit causes a proportional current to flow through transistor Q₅ to charge pulse rate capacitor C₂. The reflected current I₂ is directly proportional to the input current I₁ and is given by $I_2 = I_1 (N_1/N_2)$ where:

I₁ = current to be sensed

I₂ = reflected current

N₂ = primary turns of T₂

N₁ = secondary turns of T₂

When C₂ has accumulated sufficient charge to cause

(continued overleaf)

unijunction transistor Q₆ to conduct, an output pulse is applied to the accumulator and, simultaneously, C₂ is discharged. Thus the circuit converts the current rate to pulse rate. The accumulator indicates the total ampere-hours and consists of a simple counter. The magnetizing current required by T₂ during conduction of Q₄ causes an undesirable error at low current levels; however, its effects can be minimized by utilizing the stored energy of the square-loop core. Energy is stored during conduction of Q₃ and its level is determined by R₁. When the driving source to Q₃ is removed the energy is transferred to the capacitor C₁ via diode D₁ and the power source E₁. Thus capacitor C₁ is capable of supplying the required magnetizing current during conduction of Q₄. Resistor R₂ is adjusted to drain off the excess stored charge on C₁.

Notes:

1. This device can be used to measure the ampere-hour capacity of batteries.
2. By adding series resistors to the current-sensing coil the output pulse rate will be directly proportional to the voltage being sensed, thus making the circuit a voltage-to-frequency converter.
3. Addition of an accumulator to the voltage-to-frequency converter makes the circuit a volt-time integrator.
4. Inquiries concerning this invention may be directed to:

Technology Utilization Officer
Goddard Space Flight Center
Greenbelt, Maryland, 20771
Reference: B65-10308

Patent status: NASA encourages the immediate commercial use of this invention. Inquiries about obtaining rights for its commercial use may be made to NASA, Code AGP, Washington, D.C., 20546.

Source: John Paulkovich
(GSFC-203)